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April 2009

FIN1027 / FIN1027A — 3.3V LVDS, 2-Bit, High-Speed, Differential Driver

Description

Features

FAIRCHILD

- Greater than 600Mbs Data Rate
- 3V Power Supply Operation
- 5ns Maximum Differential Pulse Skew
- 1.5ns Maximum Propagation Delay
- Low Power Dissipation
- Power-Off Protection
- Meets or Exceeds the TIA/EIA-644 LVDS Standard
- Flow-through Pinout Simplifies PCB Layout

This dual driver is designed for high-speed interconnects utilizing Low Voltage Differential Signaling (LVDS) technology. The driver translates LVTTL signal levels to LVDS levels with a typical differential output swing of 350mV, which provides low EMI at ultra-low power dissipation, even at high frequencies. This device is ideal for high-speed transfer of clock or data.

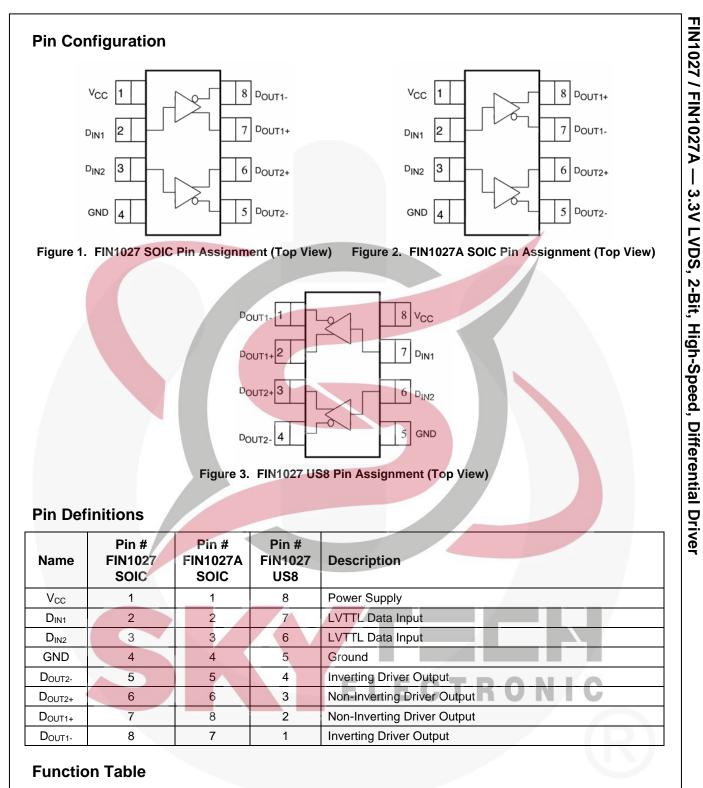
The FIN1027 or FIN1027A can be paired with its companion receiver, the FIN1028, or with any other LVDS receiver.

Ordering Information

Part Number	Operating Temperature Range	Eco Status	Package	Packing Method
FIN1027M	-40 to +85°C	Green	8-Lead Small Outline Package (SOIC) JEDEC MS-012, 0.150 inch Narrow	Trays
FIN1027MX	-40 to +85°C	Green	8-Lead Small Outline Package (SOIC) JEDEC MS-012, 0.150 inch Narrow	Tape and Reel
FIN1027K8X	-40 to +85°C	RoHS	8-Lead US8, JEDEC MO-187, Variation CA 3.1mm Wide	Tape and Reel
FIN1027AMX	-40 to +85°C	Green	8-Lead Small Outline Package (SOIC) JEDEC MS-012, 0.150 inch Narrow	Tape and Reel

Ø For Fairchild's definition of Eco Status, please visit: <u>http://www.fairchildsemi.com/company/green/rohs_green.html</u>.

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Input	Outputs		
D _{IN}	D _{out+}	D _{OUT-}	
LOW	LOW	HIGH	
HIGH	HIGH	LOW	
OPEN	LOW	HIGH	

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Min.	Max.	Unit
V _{CC}	Supply Voltage	-0.5	4.6	V
D _{IN}	DC Input Voltage	-0.5	6.0	V
D _{OUT}	DC Output Voltage	-0.5	4.7	V
I _{OSD}	Driver Short-Circuit Current	Conti	Continuous	
T _{STG}	Storage Temperature Range	-65	+150	°C
TJ	Maximum Junction Temperature		+150	
TL	Lead Temperature, Soldering, 10 Seconds		+260	°C
ESD	Human Body Model, JESD22-A114		≥6500	V
E2D	Machine Model, JESD22-A115		≥400	V

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Max.	Unit
V _{CC}	Supply Voltage	3.0	3.6	V
V _{IN}	Input Voltage	0	V _{CC}	V
T _A	Operating Temperature	-40	+85	°C

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DC Electrical Characteristics

All typical values are at $T_A = 25^{\circ}$ C and $V_{CC} = 3.3$ V. Over-supply voltage and operating temperature ranges, unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
V _{OD}	Output Differential Voltage		250	350	450	mV
ΔV_{OD}	V _{OD} Magnitude Change from Differential LOW-to-HIGH				25	mV
Vos	Offset Voltage	$R_L = 100\Omega$, Figure 4	1.125	1.250	1.375	V
ΔV_{OS}	Offset Magnitude Change from Differential LOW-to-HIGH				25	mV
I _{OFF}	Power-Off Output current	$V_{CC} = 0V$, $V_{OUT} = 0V$ or 3.6V			±20	μA
	Short-Circuit Output Current	$V_{OUT} = 0V$			-8	mA
l _{os}		$V_{OD} = 0V$			±8	
VIH	Input HIGH Voltage		2.0		Vcc	V
VIL	Input LOW Voltage		GND		0.8	V
l _{IN}	Input Current	$V_{IN} = 0V \text{ or } V_{CC}$			±20	μA
I _{I(OFF)}	Power-Off Input Current	$V_{CC} = 0V, V_{IN} = 0V \text{ or } 3.6V$			±20	μA
VIK	Input Clamp Voltage	I _{IK} = -18mA	-1.5			V
		No Load, $V_{IN} = 0V$ or V_{CC}			12.5	mA
I _{CC}	Power Supply Current	$R_L = 100\Omega$, $V_{IN} = 0V$ or V_{CC}			17.0	mA
CIN	Input Capacitance			4		pF
COUT	Output Capacitance			6		pF

AC Electrical Characteristics

All typical values are at $T_A = 25^{\circ}$ C and $V_{CC} = 3.3$ V. Over-supply voltage and operating temperature ranges, unless otherwise noted.

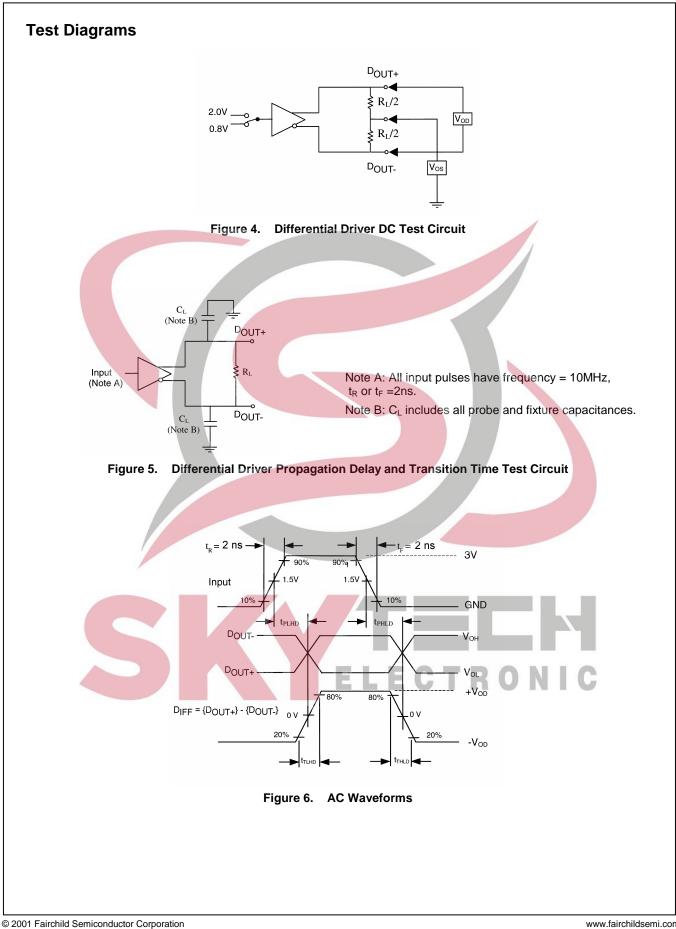
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
t _{PLHD}	Differential Propagation Delay, LOW-to-HIGH		0.5		1.5	ns
t _{PHLD}	Differential Propagation Delay, HIGH-to-LOW	ELEC	0.5		1.5	ns
t _{TLHD}	Differential Output Rise Time (20% to 80%)	$R_L = 100\Omega,$ $C_L = 10pF,$	0.4		1.0	ns
t _{тньd}	Differential Output Fall Time (80% to 20%)	Figure 5, Figure 6	0.4		1.0	ns
t _{SK(P)}	Pulse Skew t _{PLH} - t _{PHL}				0.5	ns
t _{SK(LH)} , t _{SK(HL)}	Channel-to-Channel Skew ⁽¹⁾				0.3	ns
t _{SK(PP)}	Part-to-Part Skew ⁽²⁾				1.0	ns

Notes:

1. t_{SK(LH)}, t_{SK(HL)} is the skew between specified outputs of a single device when the outputs have identical loads and are switching in the same direction.

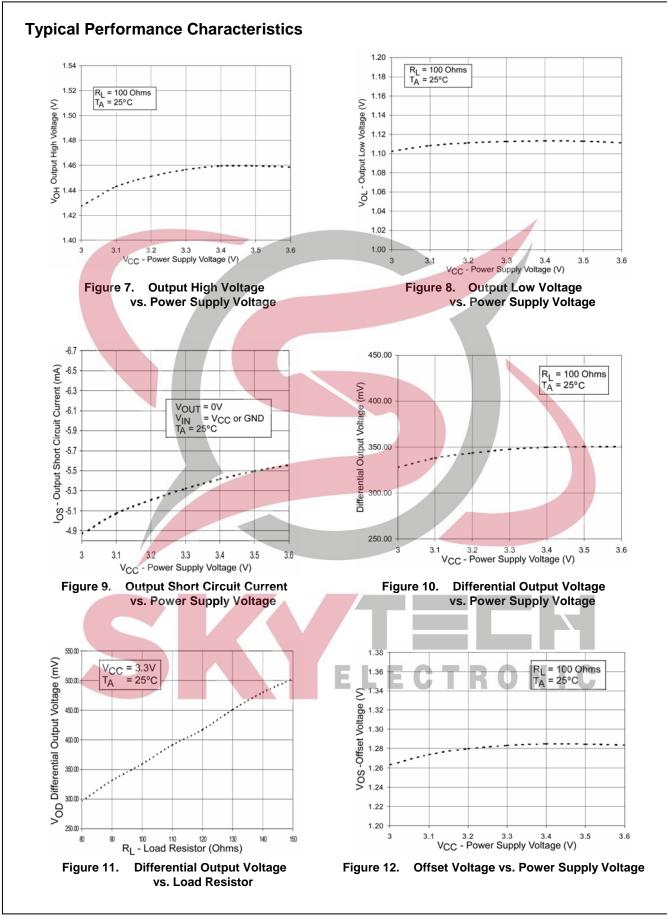
 t_{SK(PP)} is the magnitude of the difference in propagation delay times between any specified terminals of two devices switching in the same direction (either LOW-to-HIGH or HIGH-to-LOW) when both devices operate with the same supply voltage, same temperature, and have identical test circuits.

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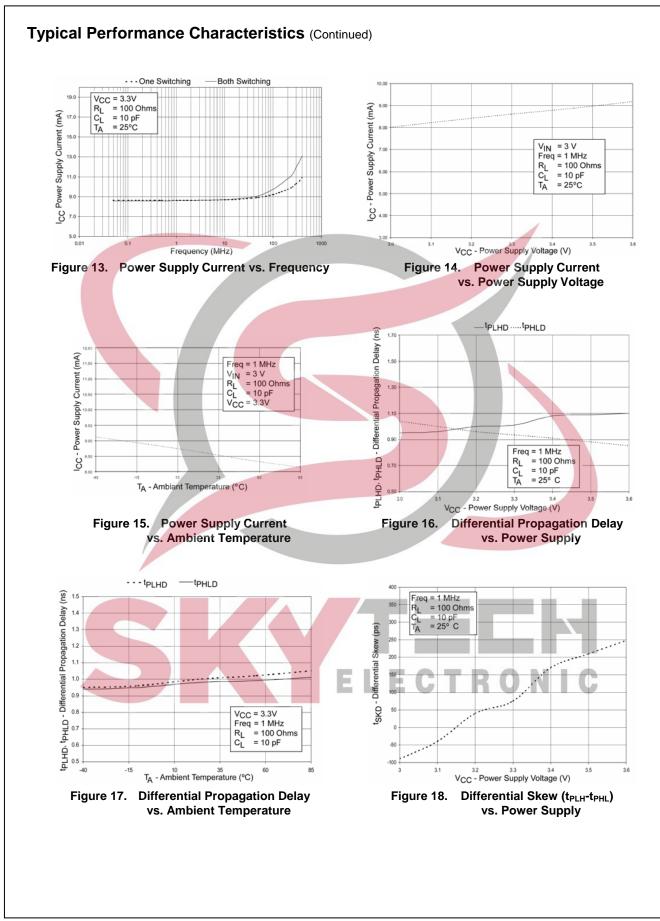


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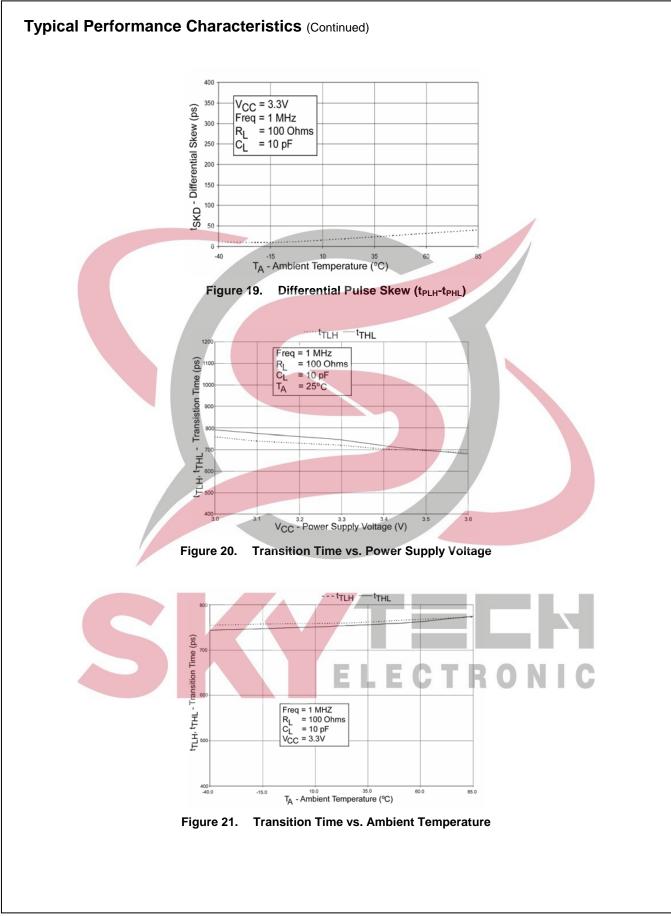
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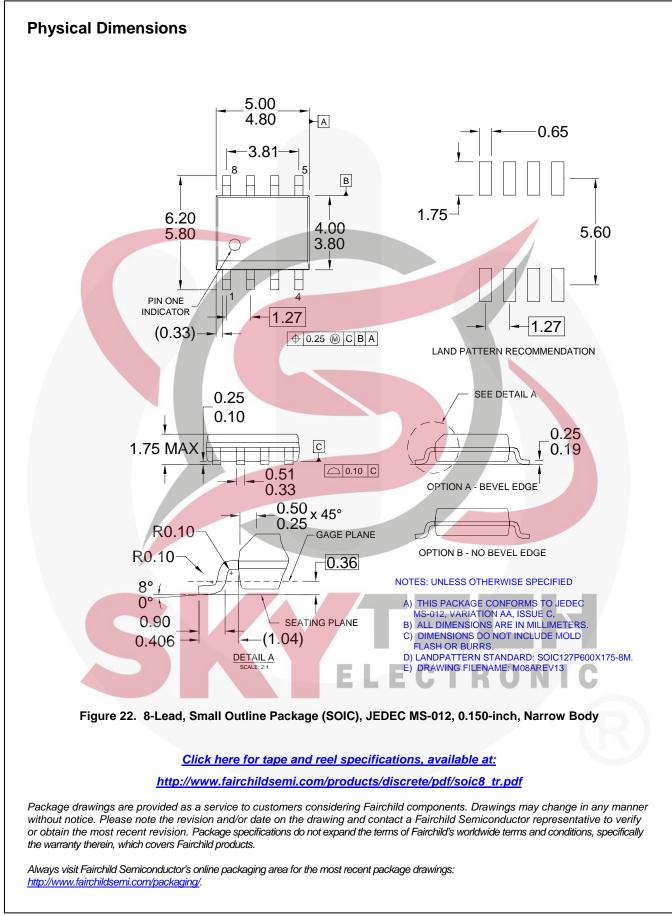
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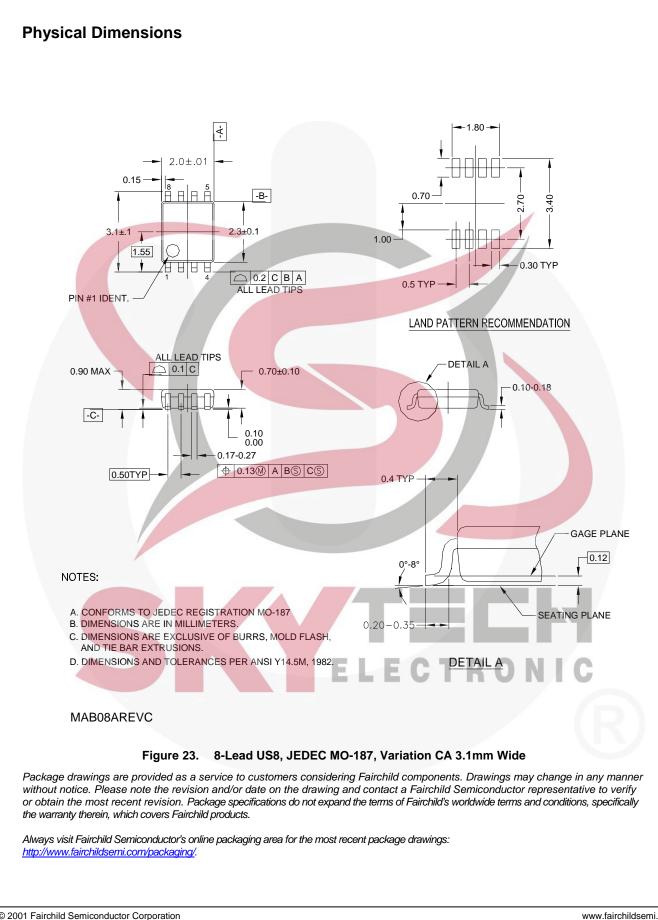
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